



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Receipt
#5
9-28-01
Mvelson

Applicant: Ko, Uming

Serial No.: 09/723,563

Filed: 01/22/2000

For: INPUT/OUTPUT ARCHITECTURE FOR
INTEGRATED CIRCUITS

Docket No.: TI-29632

Examiner: TBD

Art Unit: 2811

Confirmation No.: 5590

CORRECTION TO FILING RECEIPT

Assistant Commissioner for Patents

Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner For Patents, Washington, DC 20231.

Elizabeth Austin
Elizabeth Austin
Date: 6/29/01

Please correct the blue filing receipt to read -- This application claims priority under 35 U.S.C. §119(e)(1) of provisional application Serial No. 60/175,613 filed on 01/11/2000--. A copy of the filing receipt is enclosed with the correction noted in red.

Applicant respectfully requests that a copy of the corrected blue filing receipt be provided.

Respectfully submitted,

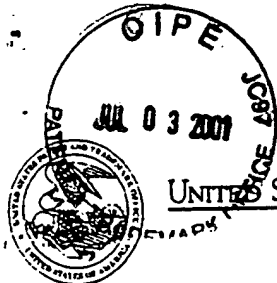
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COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/723,563	11/28/2000	2811	1248	TI-29632	5	41	5

CONFIRMATION NO. 5590

FILING RECEIPT

OC00000006032497

RONALD O. NEERINGS
Texas Instruments Incorporated
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Dallas, TX 75265

Date Mailed: 05/02/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

U-Ming Ko, Plano, TX;

Domestic Priority data as claimed by applicant – This application claims priority under 35 U.S.C. §119(e)(1) of provisional application Serial No. 60/175,613 filed on 01/11/2000.

Foreign Applications

If Required, Foreign Filing License Granted 03/28/2001

Projected Publication Date: N/A

Non-Publication Request: No

Early Publication Request: No

Title

Input/output architecture for integrated circuits

Preliminary Class

257

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Data entry by : ZEWDIE, BIZUAYEHU

Team : OIPE

Date: 05/02/2001

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